

A

B

C

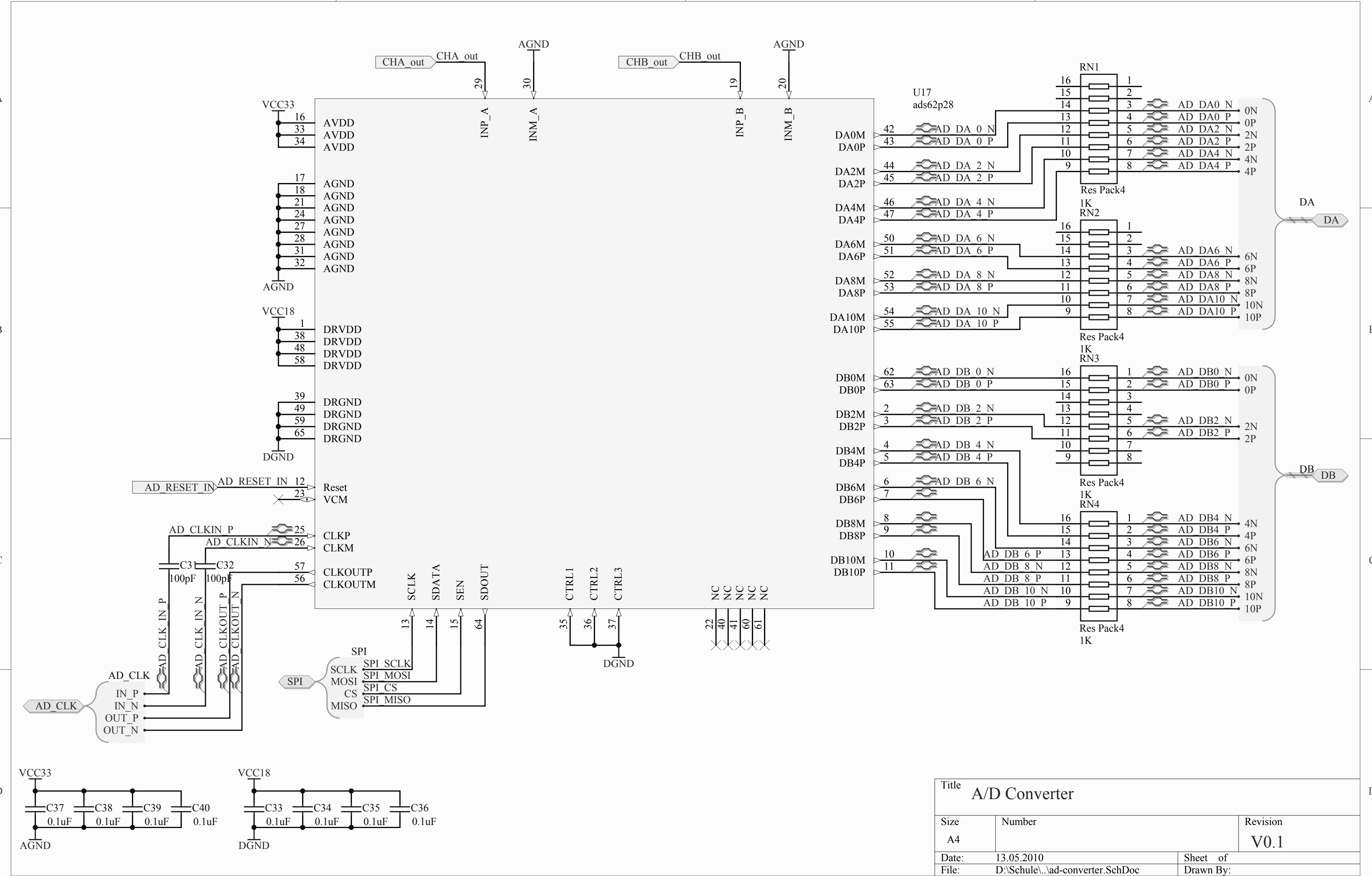
D

A

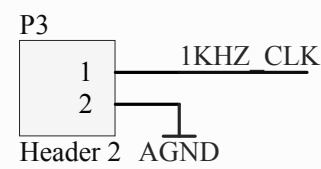
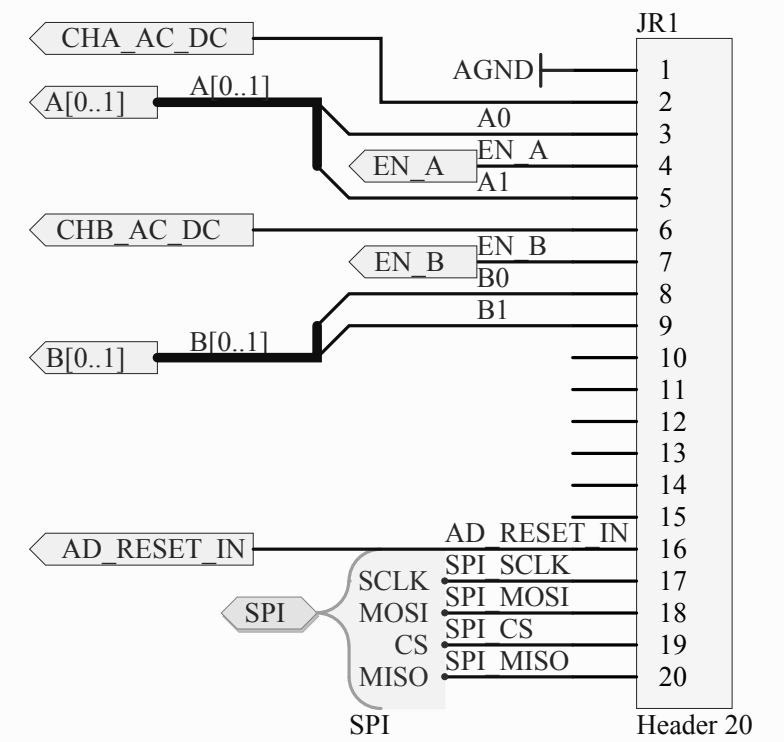
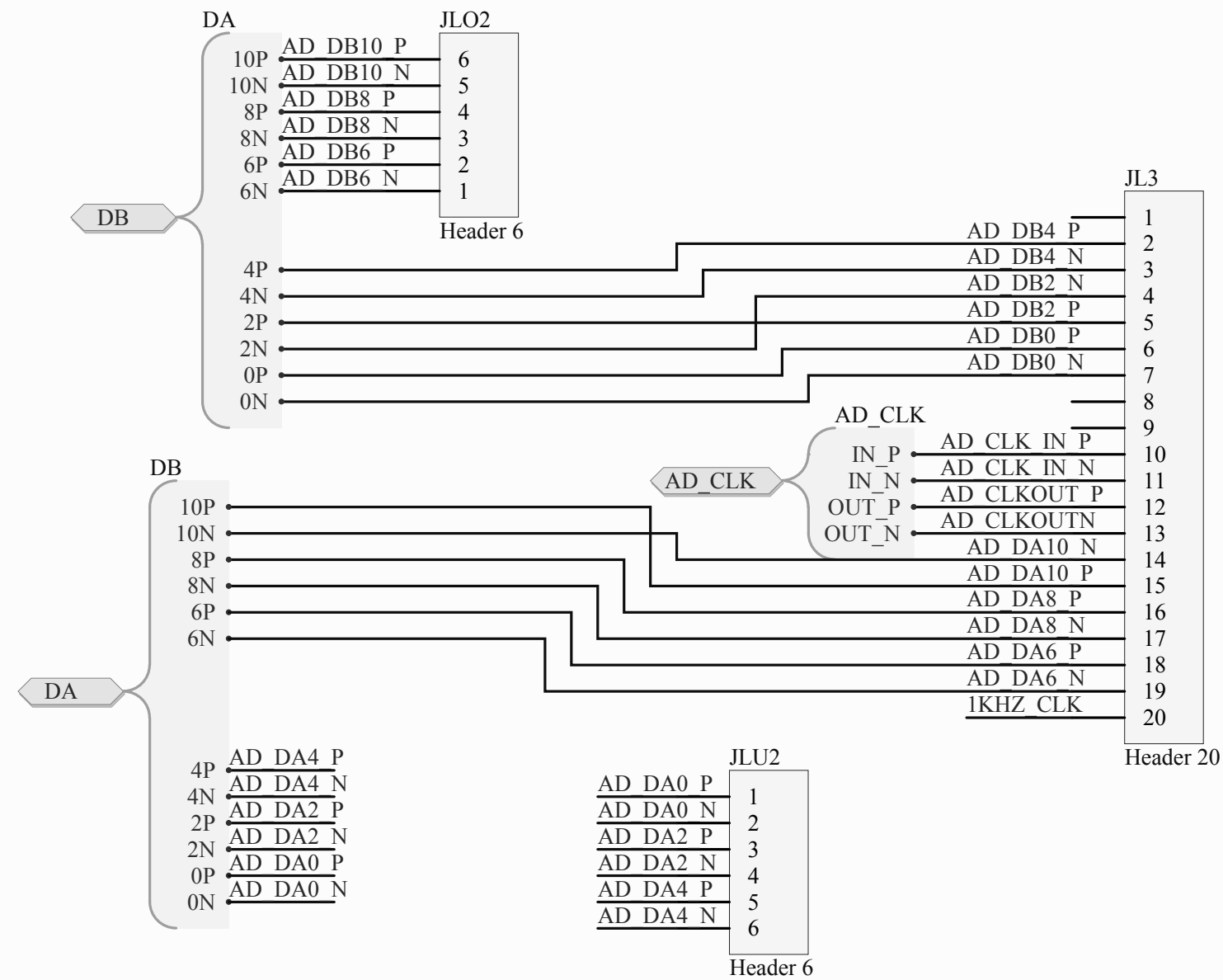
B

C

D

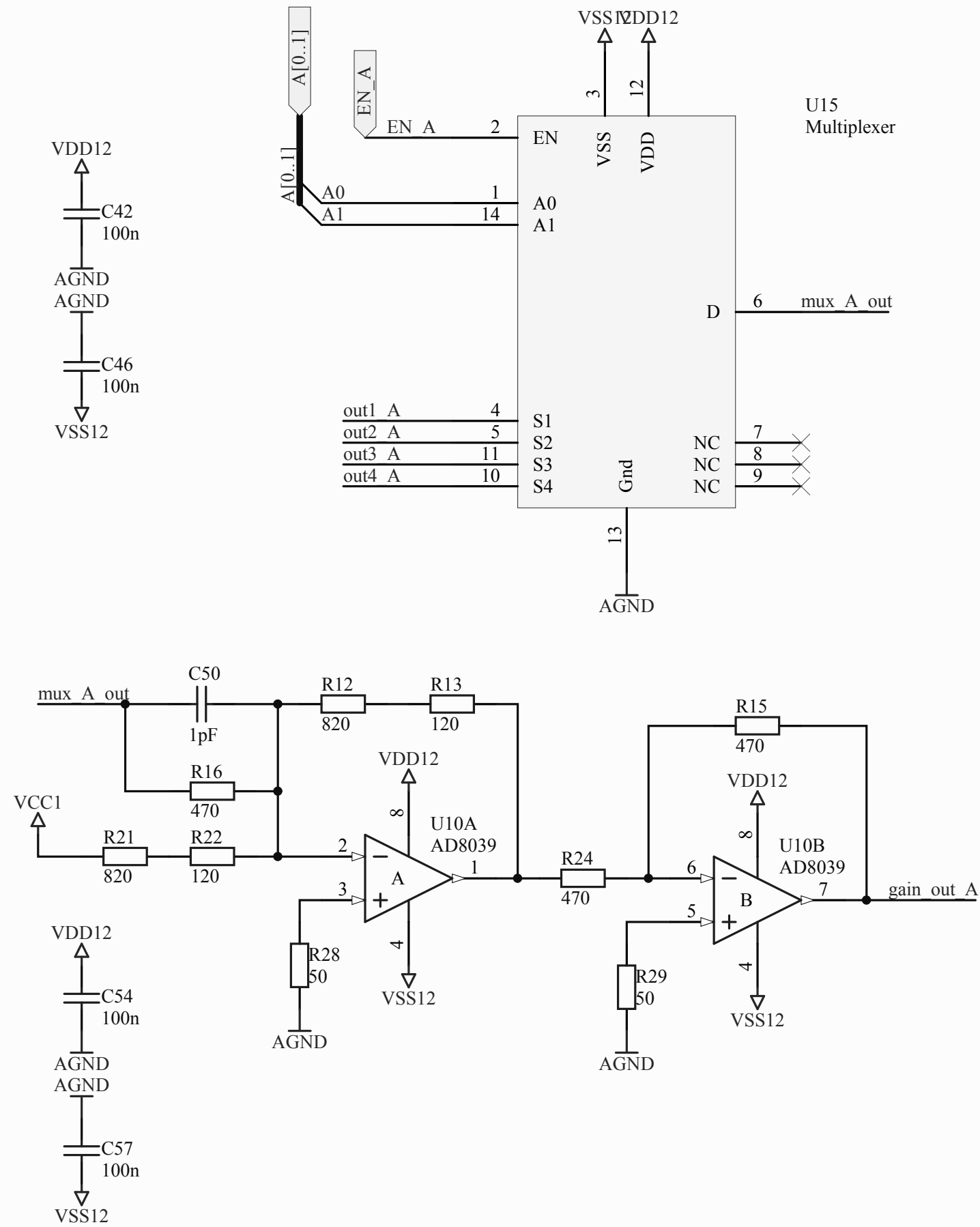


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Date:	13.05.2010	Sheet of
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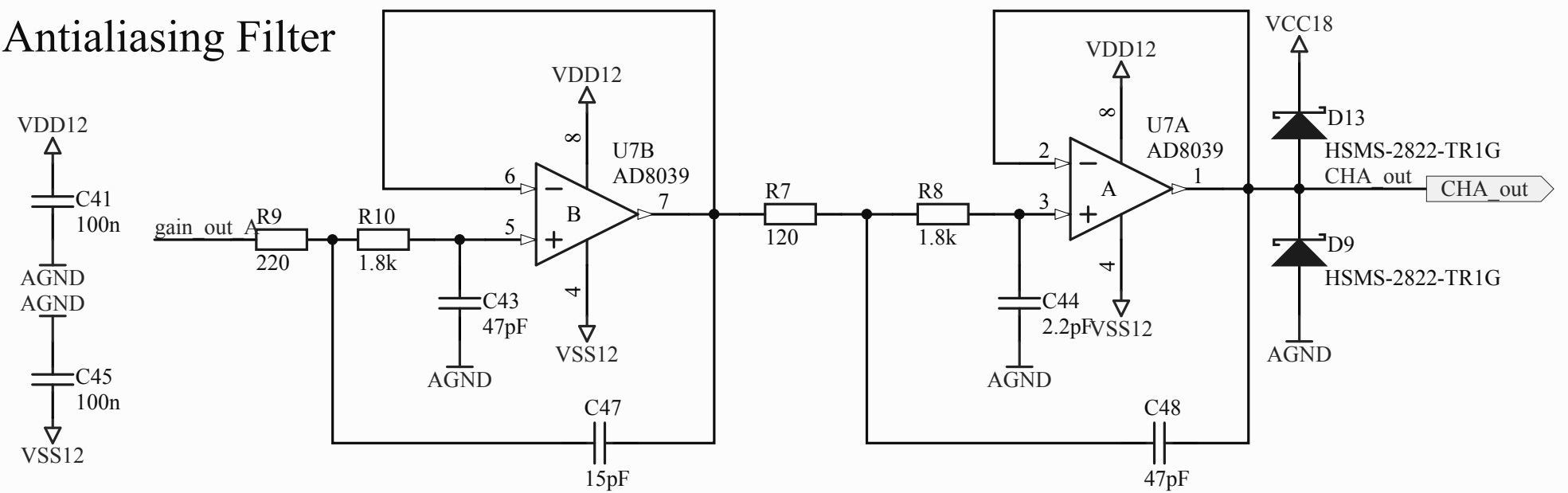


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Date:	13.05.2010	Sheet of
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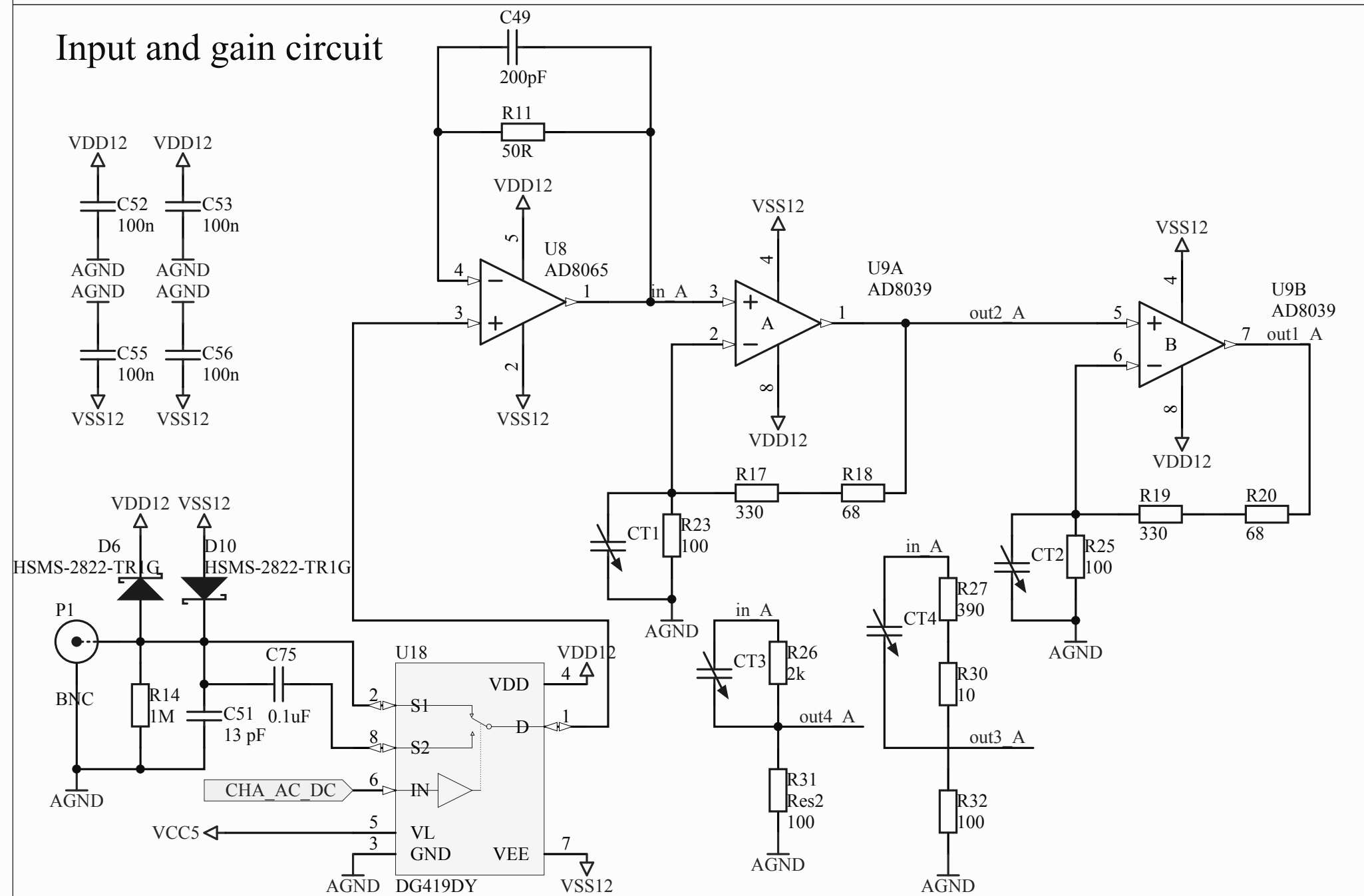
Offset voltage adder and gain control



Antialiasing Filter



Input and gain circuit

Title
Channel BSize
A4

Number

Revision
V0.1Date: 13.05.2010
File: D:\Schule\...\lcpd-scope-CHA.SchDocSheet of
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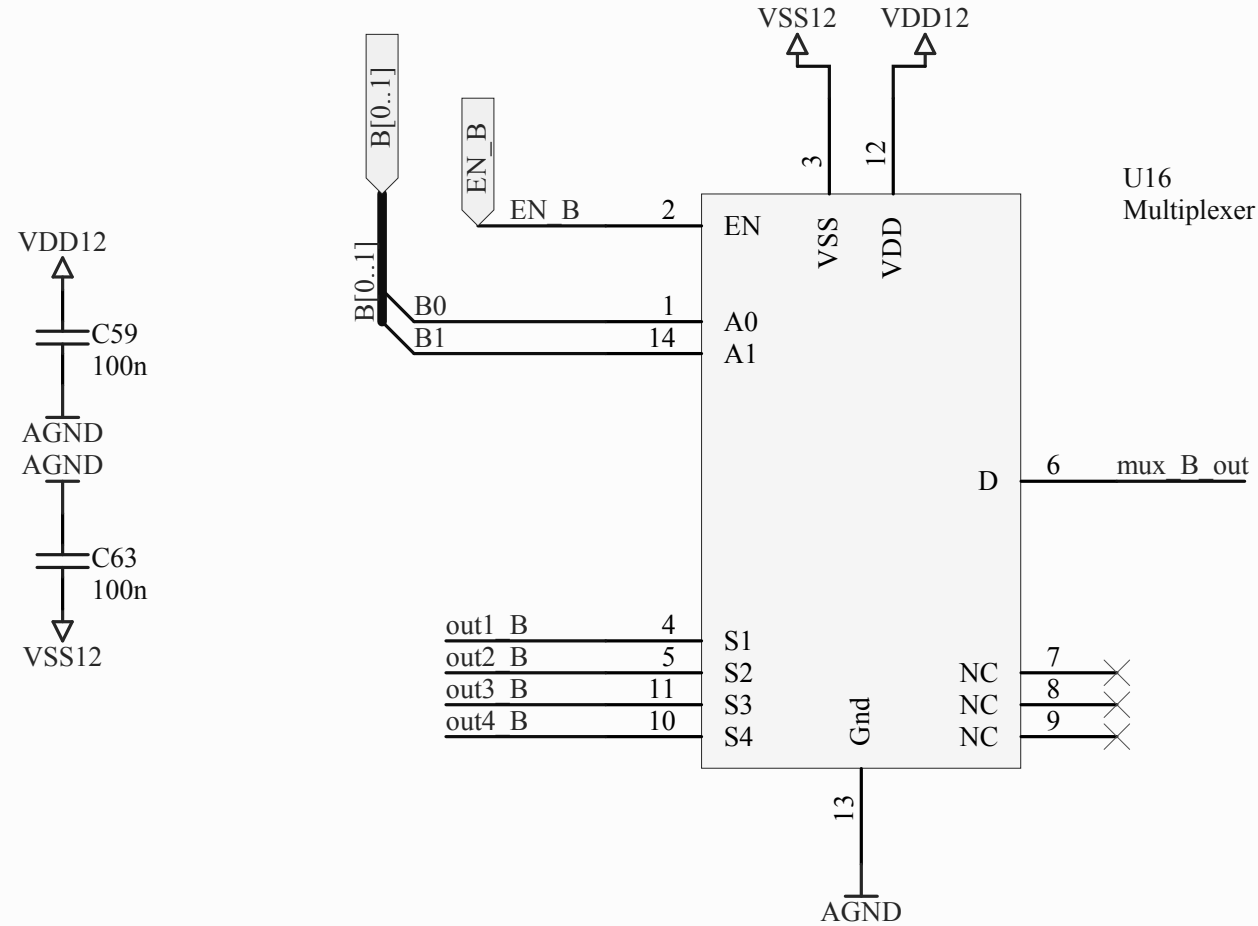
A

B

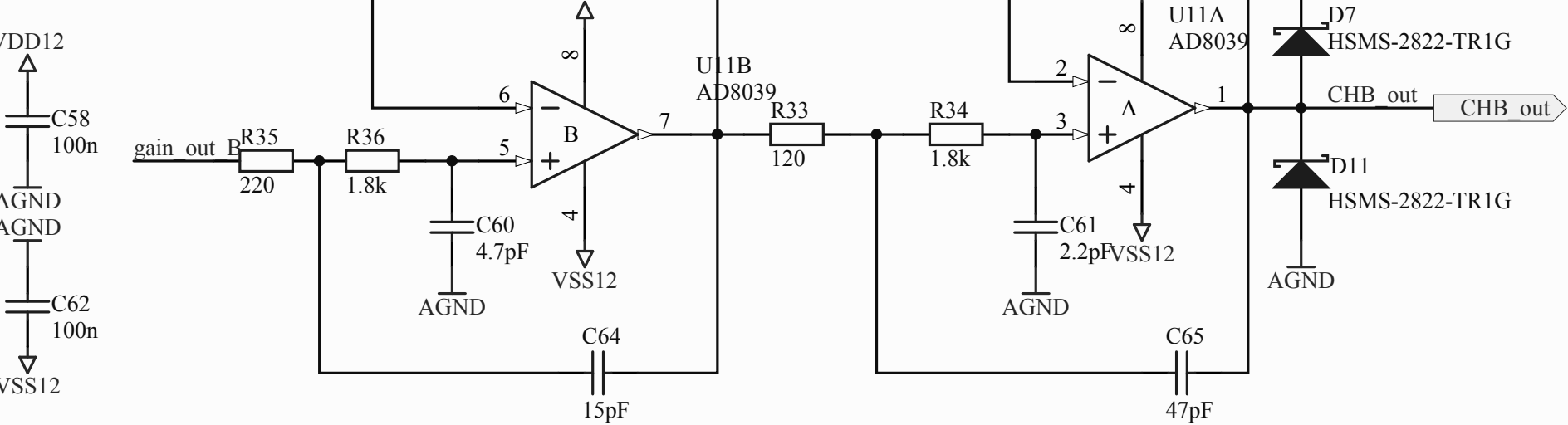
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D

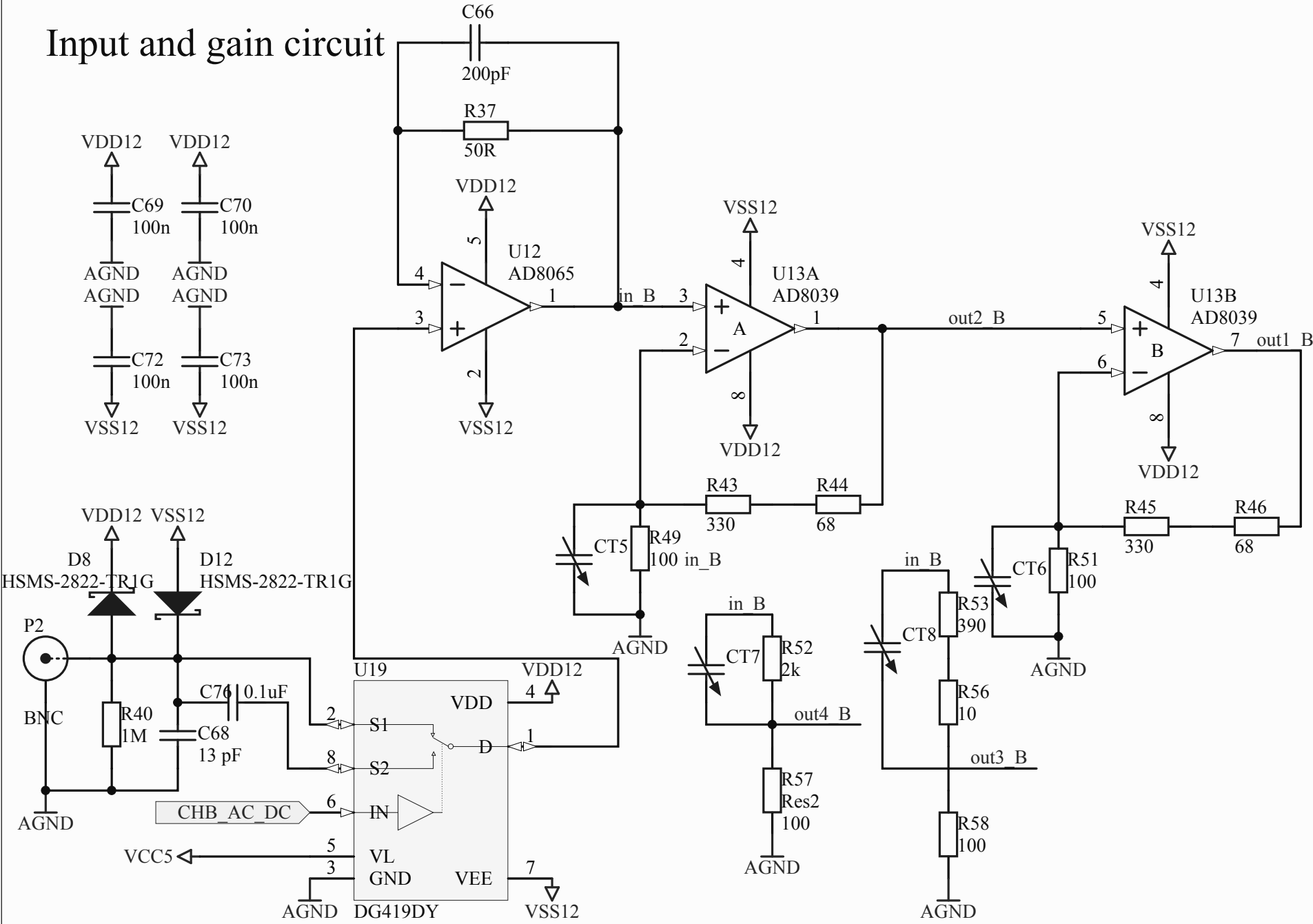
Offset voltag adder



Antialiasing Filter



Input and gain circuit



Title			Channel A
Size	Number		Revision
A4			V0.1
Date:	13.05.2010		Sheet of
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